

FISH & RICHARDSON P.C.

December 8, 2003

Attorney Docket No.: 12732-075002

Mail Stop Patent Application

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:

TR

BOSTON

DALLAS

Applicant: AKIRA ISHIKAWA

Title:

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD

THEREOF

DELAWARE

SAN DIEGO

SILICON VALLEY

TWIN CITIES

WASHINGTON, DC

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd., a Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on December 20, 2001 at 012387/0267.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	Pages
Specification	44
Claims	10
Abstract	1
Declaration	2
Drawing(s)	25

Enclosures:

- Information Disclosure Statement, 1 page
- Form PTO-1449, 1 page, listing documents cited in the parent application(s). Please confirm that these have been considered in this application by returning a copy of the Form PTO-1449 with the examiner's initials.
- Preliminary amendment, 12 pages.
- Copies of priority documents nos. 2000-280864 dated September 14, 2000 and 2000-280902 dated September 14, 2000.
- Copy of Notice of Recordation of assignment document as received in parent (7 pages). Please enter in this application.
- Serial Number Card (green).

1425 K STREET, N.W. 11TH FLOOR WASHINGTON, DC 20005



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This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 09/951,384, filed September 14, 2001, which, under 35 USC 119, claims the benefit of foreign priority applications filed in Japan on September 14, 2000 as serial numbers 2000-280864 and 2000-280902. The disclosure of the prior applications is considered part of (and is incorporated by reference in) the disclosure of this application.

Basic filing fee	\$770
Total claims in excess of 20 times \$18	\$90
Independent claims in excess of 3 times \$86	\$172
Fee for multiple dependent claims	\$0
Total filing fee:	\$1032

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171

PTO Customer Number

Respectfully submitted,

John F. Hayden Reg. No. 37,640

Enclosures

JFH/cmt

40191047.doc



ซ์.: 12732-075001 / US5208/5209

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Akira Ishikawa

Art Unit: 2811

Serial No.: 09/951,384

Examiner: Unknown

Filed

: September 14, 2001

Title

: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

U.S. Patent and Trademark Office **Assignment Division** Box Assignments—CG-4 1213 Jefferson Davis Highway Suite 320 Washington, D.C. 20231

RESPONSE TO NOTICE OF NON-RECORDATION OF DOCUMENT

In response to the Notice of Non-Recordation of Document mailed November 20, 2001, Applicant submits herewith a copy of the notice and a recordation cover sheet identifying the application number of the instant application. A copy of the official filing receipt is attached for verification of the same. A notice of recordation is respectfully requested.

No fee is believed to be due. Please apply any charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: December 20, 2001

John F. Hayden Reg. No. 37,640

Fish & Richardson P.C.

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